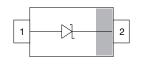


SD101AW, SD101BW, SD101CW

Vishay Semiconductors

Small Signal Schottky Diodes





LINKS TO ADDITIONAL RESOURCES











MECHANICAL DATA

Case: SOD-123

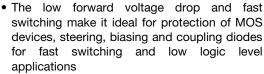
Weight: approx. 10.6 mg Packaging codes/options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

FEATRUES









 The SD101 series is a metal-on-silicon Schottky barrier device which is protected by a PN junction guardring



COMPLIANT

- AEC-Q101 qualified available
- Molding compound meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level (MSL) 1
- Base P/N-E3 RoHS-compliant, commercial grade
- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

PARTS TABLE							
PART	ORDERING CODE	AEC-Q101 QUALIFIED	TYPE MARKING	CIRCUIT CONFIGURATION	TAPED UNITS PER REEL	MINIMUM ORDER QUANTITY	
SD101AW	SD101AW-E3-08	no		Single	3 000	15 000	
	SD101AW-HE3_A-08	yes	SK		(8 mm tape on 7" reel)		
	SD101AW-E3-18	no	SK		10 000	10 000	
	SD101AW-HE3_A-18	yes			(8 mm tape on 13" reel)		
SD101AW	SD101BW-E3-08	no		Single	3 000	15 000	
	SD101BW-HE3_A-08	yes	SL		(8 mm tape on 7" reel)		
	SD101BW-E3-18	no	SL		10 000	10 000	
	SD101BW-HE3_A-18	yes			(8 mm tape on 13" reel)		
SD101CW	SD101CW-E3-08	no			3 000	15 000	
	SD101CW-HE3_A-08	yes	SM	Single	(8 mm tape on 7" reel)		
	SD101CW-E3-18	no		Single	10 000	10 000	
	SD101CW-HE3_A-18	yes	Ī		(8 mm tape on 13" reel)		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
		SD101AW	V_{RRM}	60	V	
Repetitive peak reverse voltage		SD101BW	V_{RRM}	50	V	
		SD101CW	V_{RRM}	40	V	
Power dissipation	on FR-4 board with recommended soldering footprint		D	230	mW	
	Infinite heatsink		P _{tot}	330	mW	
Forward continuous current (1)			I _F	100	mA	
Maximum single cycle surge	10 μs square wave		I _{FSM}	2	Α	

Note

(1) Infinite heatsink



SD101AW, SD101BW, SD101CW

Vishay Semiconductors

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air	according to JEDEC® 51-3 on FR-4 board with recommended soldering footprint	R _{thJA}	420	K/W		
Thermal resistance junction lead	Infinite heatsink	R _{thJL}	300	K/W		
Maximum junction temperature		T _j	125	°C		
Storage temperature range		T _{stg}	-65 to +150	°C		
Operating temperature range		T _{op}	-55 to +150	°C		

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
	I _R = 10 μA	SD101AW	V _(BR)	60			V
Reverse breakdown voltage		SD101BW	V _(BR)	50			V
		SD101CW	V _(BR)	40			V
	V _R = 50 V	SD101AW	I _R			200	nA
Leakage current	V _R = 40 V	SD101BW	I _R			200	nA
	V _R = 30 V	SD101CW	I _R			200	nA
	I _F = 1 mA	SD101AW	V _F			410	mV
		SD101BW	V_{F}			400	mV
Company voltage dress		SD101CW	V_{F}			390	mV
Forward voltage drop	I _F = 15 mA	SD101AW	V _F			1000	mV
		SD101BW	V_{F}			950	mV
		SD101CW	V_{F}			900	mV
Diode capacitance	V _R = 0 V, f = 1 MHz	SD101AW	C _D			2	pF
		SD101BW	C _D			2.1	pF
		SD101CW	C _D			2.2	pF
Reverse recovery time	$I_F = I_R = 5$ mA, recover to 0.1 I_R		t _{rr}			1	ns

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TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

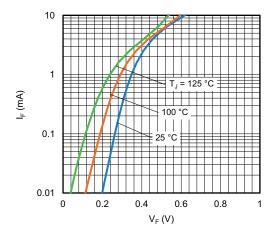


Fig. 1 - Typical Forward Current vs. Forward Voltage

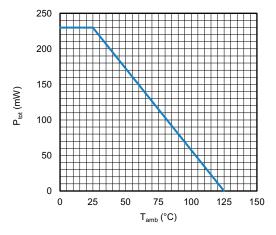


Fig. 2 - Admissible Power Dissipation vs. Ambient Temperature

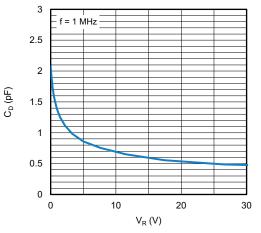


Fig. 3 - Typical Capacitance vs. Reverse Voltage

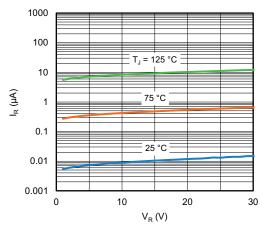


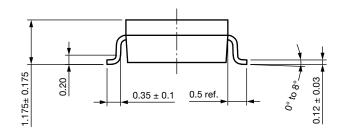
Fig. 4 - Typical Reverse Leakage vs. Reverse Voltage

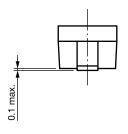


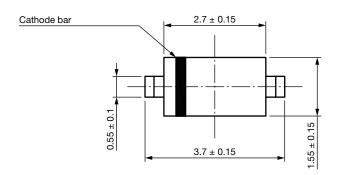


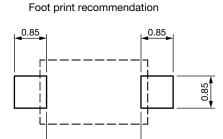
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PACKAGE DIMENSIONS in millimeters (inches): SOD-123









2.5

Rev. 01 - Date: 18. Jan. 2022 Document no.: S8-V-3910.01-003 (4)

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 0.203 ± 0.013

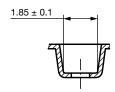
 3.94 ± 0.1

1.57 ± 0.1

CARRIER TAPE SOD-123

A - A section O1.55 ± 0.05 A + ± 0.1 B B B A

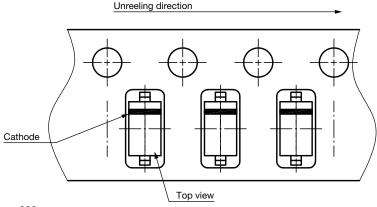
B - B section



Rev. 02 - Date: 21. Jan. 2014 Document no.: S8-V-3717.10-002 (4)

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ORIENTATION IN CARRIER TAPE SOD-123



Rev. 02 - Date: 07. Nov. 2022 Document no.: S8-V-3717.10-003 (4)

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