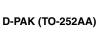
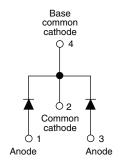


Vishay Semiconductors

Schottky Rectifier, 2 x 3.5 A



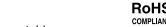




PRODUCT SUMMARY				
Package	D-PAK (TO-252AA)			
I _{F(AV)}	2 x 3.5 A			
V_{R}	60 V			
V _F at I _F	See Electrical table			
I _{RM}	30 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Common cathode			
E _{AS}	6 mJ			

FEATURES

- Popular D-PAK outline
- Center tap configuration



- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- \bullet Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

DESCRIPTION

The VS-6CWQ06FNPbF surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	7	A			
V _{RRM}		60	V			
I _{FSM}	t _p = 5 μs sine	490	Α			
V _F	3 Apk, T _J = 25 °C (per leg)	0.61	V			
T _J	Range	- 40 to 150	°C			

VOLTAGE RATINGS						
PARAMETER SYMBOL VS-6CWQ06FNPbF UNITS						
Maximum DC reverse voltage	V_{R}	60	V			
Maximum working peak reverse voltage V _{RWM} 60 V						

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average per leg		50 % duty cycle at T _C = 133 °C, rectangular waveform		50 0/ duty quals at T 122 °C restangula	3.5		
See fig. 5 per device	I _{F(AV)}			7	Α		
Maximum peak one cycle non-repetitive surge current		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	490	^		
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	70			
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 12 mH		6	mJ		
Repetitive avalanche current per leg I_{AR} Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		1	Α				

VS-6CWQ06FNPbF

Vishay Semiconductors

Schottky Rectifier, 2 x 3.5 A



Document Number: 94249

Revision: 14-Jan-11

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
		3 A	T _{.I} = 25 °C	0.61	V	
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	6 A	1j = 25 °C	0.76		
See fig. 1	VFM (1)	3 A	T _{.I} = 125 °C	0.53		
g		6 A	1J = 123 O	0.65		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	2	· mA	
See fig. 2	'RM`'	T _J = 125 °C	VR = Mateu VR	30		
Threshold voltage	$V_{F(TO)}$	T T		0.38	V	
Forward slope resistance	r _t	$T_J = T_J$ maximum	34.31	mΩ		
Typical junction capacitance per leg	C _T	$V_R = 5 V_{DC}$, (test signal ran	145	pF		
Typical series inductance per leg	L _S	Measured lead to lead 5 m	5.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs		

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range		T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C		
Maximum thermal resistance,	per leg	В	DC operation	4.70	°C/W		
junction to case per dev	per device	R_{thJC}	See fig. 4	2.35	C/VV		
Approximate weight				0.3	g		
Approximate weight				0.01	OZ.		
Marking device			Case style D-PAK (similar to TO-252AA)	6CWC	Q06FN		

Note



Schottky Rectifier, 2 x 3.5 A

Vishay Semiconductors

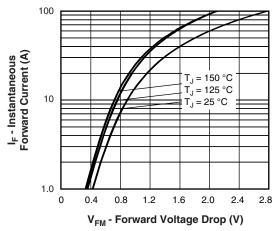


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

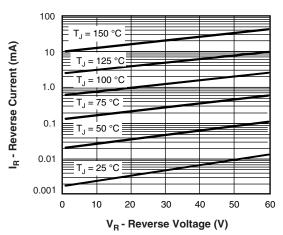


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

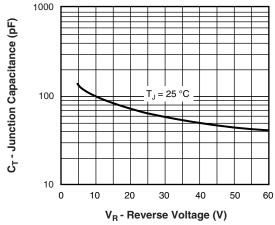


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

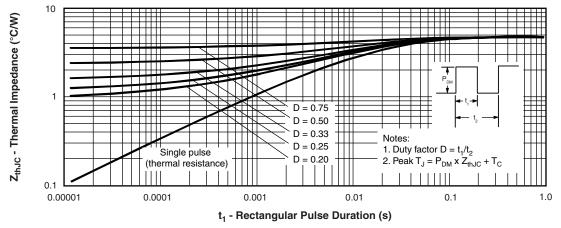
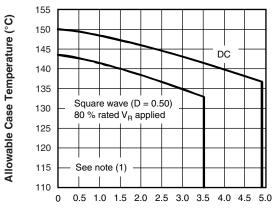


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

Vishay Semiconductors

Schottky Rectifier, 2 x 3.5 A





I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

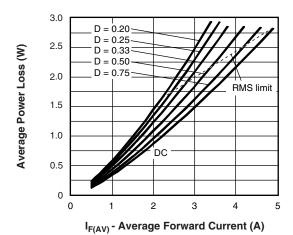


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

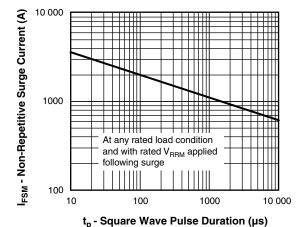


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \text{ (1 - D); } I_R \text{ (2 - D); } I_R \text{ (3 - D); } I_R \text{ (2 - D); } I_R \text{ (3 - D); } I_R \text{ (3 - D); } I_R \text{ (4 - D)$

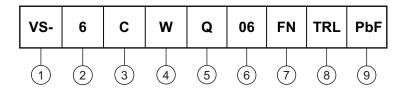


Schottky Rectifier, 2 x 3.5 A

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



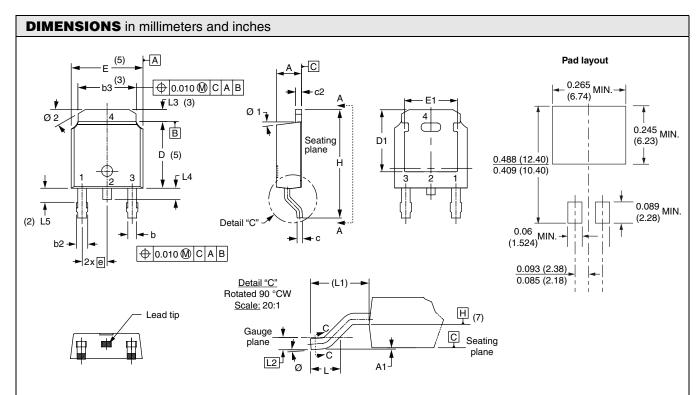
- 1 Vishay Semiconductors product
- 2 Current rating (7 A)
- Center tap configuration
- Package identifier:
 - W = D-PAK
- 5 Schottky "Q" series
- Voltage rating (06 = 60 V)
- 7 FN = TO-252AA (D-PAK)
- None = Tube (50 pieces)
 - TR = Tape and reel
 - TRL = Tape and reel (left oriented)
 - TRR = Tape and reel (right oriented)
- 9 PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95016			
Part marking information	www.vishay.com/doc?95059			
Packaging information	www.vishay.com/doc?95033			



Vishay High Power Products

D-PAK (TO-252AA)



SYMBOL	MILLIM	MILLIMETERS		INCHES		
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	2.18	2.39	0.086	0.094		
A1	-	0.13	-	0.005		
b	0.64	0.89	0.025	0.035		
b2	0.76	1.14	0.030	0.045		
b3	4.95	5.46	0.195	0.215	3	
С	0.46	0.61	0.018	0.024		
c2	0.46	0.89	0.018	0.035		
D	5.97	6.22	0.235	0.245	5	
D1	5.21	-	0.205	1	3	
Е	6.35	6.73	0.250	0.265	5	
E1	4.32	-	0.170	-	3	

SYMBOL	MILLIMETERS		INC	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29	BSC	0.090	BSC	
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74 BSC		0.108 REF.		
L2	0.51	0.51 BSC		0.020 BSC	
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
Ø	0°	10°	0°	10°	
Ø1	0°	15°	0°	15°	
Ø2	25°	35°	25°	35°	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- (5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (6) Dimension b1 and c1 applied to base metal only
- $^{(7)}$ Datum A and B to be determined at datum plane H
- (8) Outline conforms to JEDEC outline TO-252AA





Vishay

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