2.2mΩ, 60V, N-Channel Power MOSFET SRT06N022H

General Description

The Sanrise SRT06N022H is a low voltage power MOSFET, fabricated using advanced split gate trench technology. The resulting device has extremely low on resistance, low gate charge and fast switching time, making it especially suitable for applications which require superior power density and synchronous rectification.

The SRT06N022H break down voltage is 60V and it has a high rugged avalanche characteristics. The SRT06N022H is available in PDFN5*6 and TO-263-2 and TO-220C packages.

Features

- Ultra Low

$R_{DS(ON)\_TYP}$ = 1.65mΩ ,PDFN5*6 @V_{GS} = 10V.

$R_{DS(ON)\_TYP}$ = 2.2mΩ ,TO-263-2 @V_{GS} = 10V.

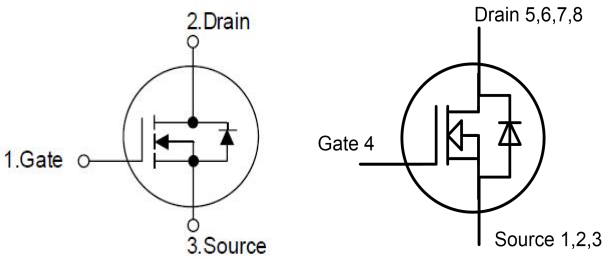
$R_{DS(ON)\_TYP}$ = 2.4mΩ, TO-220C @V_{GS} = 10V.

- Ultra Low Gate Charge, Qg=80nC typ.
- Fast switching capability
- Robust design with better EAS performance
- EMI Improved
- Non-automotive Qualified

Application

- Server/Telecom
- High Power Supply
- E-Tools
- Motor Driver
- BMS

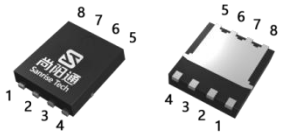
Symbol



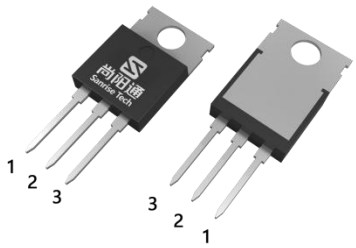
TO-220C,TO-263-2 PDFN5*6

Figure 1 Symbol of SRT06N022H

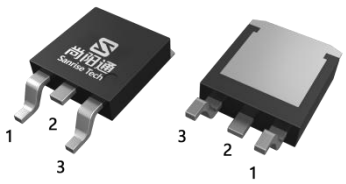
Package Type



PDFN5*6



TO-220C



TO-263-2

Figure 2 Package Type of SRT06N022H

2.2mΩ, 60V, N-Channel Power MOSFET **SRT06N022H**

Ordering Information

SRT06N022H□□–□

Circuit Type

Package
D56: PDFN5*6
S2: TO-263-2
TC: TO-220C

E: Lead Free
G: Green

Blank: Tube
TR: Tape & Reel

Package	Part Number	Marking ID	Packing Type
PDFN5*6	SRT06N022HD56TR-G	SRT06N022HD56G	Tape & Reel
TO-263-2	SRT06N022HS2TR-E	SRT06N022HS2E	Tape & Reel
TO-220C	SRT06N022HTC-G	SRT06N022HTCG	Tube

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Absolute Maximum Ratings

Parameter		Symbol	Rating		Unit
Drain-Source Voltage		V_{DSS}	60		V
Gate-Source Voltage		V_{GSS}	±20		V
Continuous Drain Current, Package Limited	T_C=25ºC	I_D	PDFN5*6	120	A
			TO-263-2	120	
			TO-220C	120	
Continuous Drain Current, Package Limited	T_C=125ºC		PDFN5*6	87	
			TO-263-2	84	
			TO-220C	84	
Continuous Drain Current, Package Limited	T_C=100ºC		PDFN5*6	120	
			TO-263-2	120	
			TO-220C	120	
Continuous Drain Current, Silicon	T_C=25ºC		PDFN5*6	194	
			TO-263-2	189	
			TO-220C	170	
Pulsed Drain Current (Note 2)		I_{DM}	PDFN5*6	600	
			TO-263-2	600	
			TO-220C	600	
Power Dissipation (T_C = 25°C)		P_D	147		W
Avalanche Destructive Energy, Single Pulse (Note 4)		E_{AS_Limit}	506		mJ
Avalanche Energy, Single Pulse (Note 3)		E_{AS}	110		mJ
Avalanche Energy, Repetitive (Note 2)		E_{AR}	0.1		mJ
Avalanche Current, Repetitive (Note 2)		I_{AR}	21		A
Operating Junction Temperature		T_J	150		ºC
Storage Temperature		T_{STG}	-55 to 150		ºC
Lead Temperature (Soldering, 10 sec)		T_{LEAD}	260		ºC

Note:

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. I_{AS}= 21.0A, V_{DD}= 30V, R_G= 25Ω, Starting T_J= 25°C
4. I_{AS_Limit}= 45A, V_{DD} = 30V, R_G = 25Ω, Starting T_J = 25°C

Thermal Resistance

Parameter		Symbol	Min	Typ	Max	Unit
Thermal Resistance，Junction-to-Case	PDFN5*6	R_{thJC}			0.85	ºC/W
Thermal Resistance，Junction-to-Ambient		R_{thJA}			50	
Thermal Resistance，Junction-to-Case	TO-263-2	R_{thJC}			0.75	ºC/W
Thermal Resistance，Junction-to-Ambient		R_{thJA}			62	
Thermal Resistance，Junction-to-Case	TO-220C	R_{thJC}			0.75	ºC/W
Thermal Resistance，Junction-to-Ambient		R_{thJA}			62	

Electrical Characteristics

T_J = 25℃, unless otherwise specified.

Parameter		Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D=250uA	60			V
Zero Gate Voltage Drain Current		I_{DSS}	V_{DS}=60V, V_{GS}=0V			1	uA
Gate-Body Leakage Current	Forward	I_{GSSF}	V_{GS}=20V, V_{DS}=0V			200	nA
	Reverse	I_{GSSR}	V_{GS}=-20V, V_{DS}=0V			-200	
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=0.25mA	2.0	3.0	4.0	V
Static Drain-Source On-Resistance	PDFN5*6	$R_{DS(ON)}$	V_{GS}=10V, I_D=30A		1.65	2.0	mΩ
	TO-263-2		V_{GS}=10V, I_D=30A		2.2	2.4	mΩ
	TO-220C		V_{GS}=10V, I_D=30A		2.4	3.0	mΩ
Gate Resistance		R_G	f=1MHz, Open Drain		1.0		Ω
Dynamic Characteristics							
Input Capacitance		C_{ISS}	V_{DS}=30V, V_{GS}=0V, f=1MHz		5.7		nF
Output Capacitance		C_{OSS}			1.9		nF
Reverse Transfer Capacitance		C_{RSS}			110		pF
Effective output capacitance, energy related NOTE5		$C_{O(er)}$	V_{GS}=0V, V_{DS}=0…48V		1.9		nF
Effective output capacitance, time related NOTE6		$C_{O(tr)}$			2.4		
Turn-on Delay Time		$t_{d(on)}$	V_{DD}=30V, I_D=30A R_G=1.6Ω, V_{GS}=10V		17		ns
Rise Time		t_r			10		
Turn-off Delay Time		$t_{d(off)}$			36		
Fall Time		t_f			8		
Gate Charge Characteristics							
Gate to Source Charge		Q_{gs}	V_{DD}=30V, I_D=30A V_{GS}=0 to 10V		27		nC
Gate to Drain Charge		Q_{gd}			16		
Gate Charge Total		Q_g			80		
Gate Plateau Voltage		$V_{plateau}$			4.7		V
Gate Charge Total, sync FET		Q_g	V_{DD}=0.1V, V_{GS}=0 to 10V		69		nC
Reverse Diode Characteristics							
Drain-Source Diode Forward Voltage		V_{SD}	V_{GS}=0V, I_{SD}=30A		0.79	1.0	V
Reverse Recovery Time		t_{rr}	V_R=30V, I_F=30A dI$_F$/dt=100A/us		53		ns
Reverse Recovery Charge		Q_{rr}			61		nC
Peak Reverse Recovery Current		I_{rrm}			2.3		A

Note:

5. $C_{O(er)}$ is a fixed capacitance that gives the same stored energy as C_{OSS} while V_{DS} is rising from 0 to 48V
6. $C_{O(tr)}$ is a fixed capacitance that gives the same charging time as C_{OSS} while V_{DS} is rising from 0 to 48V

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Sanrise Technology Limited Company

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