

100A 650V Trench Fieldstop IGBT with anti-parallel diode SRE100N065FSU2DB
General Description

The SRE100N065FSU2DB is a Field Stop Trench IGBT with anti-parallel diode, which offers ultra-low conduction loss, high energy efficiency for switching applications such as Inverter, PFC, Converter, etc.

The SRE100N065FSU2DB package is TO-247.

Features

- High Breakdown Voltage to 700V@T_j=25°C
- Advanced Trench Fieldstop technology
 - Smooth Switching Off with Lower Spike
 - High Ruggedness, Temperature Stability
 - Easy Parallel Switching Capability due to Positive Temperature Coefficient in V_{CE(SAT)}
- Low V_{CE(SAT)}
- Enhanced Avalanche Capability
- Non-Automotive Qualified

Application

- PFC application
- Inverter & Solar
- Converter with high switching frequency

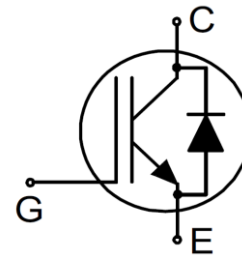
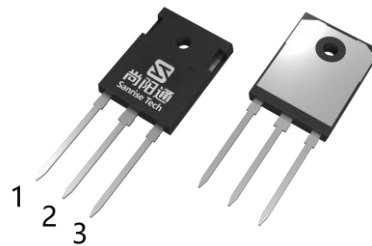
Symbol


Figure 1 Symbol of SRE100N065FSU2DB

Package Type


TO-247

Pin 1- Gate

Pin 2&backside- Collector

Pin 3-Emitter

Figure 2 Package Type of SRE100N065FSU2DB

Ordering Information

SRE100N065FSU2DB □ □ - □

Circuit Type _____ Package _____ T: TO-247	E: Lead Free Blank: Tube TR: Tape & Reel
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Package	Part Number	Marking ID	Packing Type
TO-247	SRE100N065FSU2DBT-G2	SRE100N065FSU2DBT-G2	Tube

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Absolute Maximum Ratings

Parameter		Symbol	Rating	Unit
Collector-emitter voltage		V_{CES}	650	V
Gate-emitter Voltage		V_{GES}	± 20	V
Transient Gate-emitter Voltage			± 30	V
Continuous Collector Current	$T_C=25^\circ\text{C}$	I_C	150	A
	$T_C=100^\circ\text{C}$		100	
Pulsed Collector Current, Limited by T_{Jmax}		I_{CM}	400	A
Diode Continuous Collector Current	$T_C=25^\circ\text{C}$	I_F	120	A
	$T_C=100^\circ\text{C}$		100	A
Diode Pulsed Current, Limited by T_{Jmax}		I_{FM}	320	A
Power Dissipation	$T_C=25^\circ\text{C}$	P_{tot}	394	W
	$T_C=100^\circ\text{C}$		197	
Short Circuit withstand time: $V_{GE}=15\text{V}, V_{CC} \leq 400\text{V}, T_{j_start}=25^\circ\text{C};$ Allow number of short circuits < 1000; Time between short circuits: 1.0S;		tsc	6	us
Operating Junction Temperature Range		T_J	$-40 \sim 175^{(1)}$	$^\circ\text{C}$
Storage Temperature		T_{STG}	$-55 \sim 150$	$^\circ\text{C}$
Lead Temperature (Soldering, 10 sec)		T_{LEAD}	260	$^\circ\text{C}$

Note:

1. Reliability testing conducted at $T_{Jmax}=175^\circ\text{C}$.

Thermal Resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
IGBT thermal Resistance, Junction-to-Case	R_{thJC}	-	-	0.38	$^\circ\text{C}/\text{W}$
Diode thermal Resistance, Junction-to-Case	R_{thJC}	-	-	0.45	
Thermal Resistance, Junction-to-Ambient	R_{thJA}	-	-	40	

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Electrical Characteristics
 $T_J = 25^\circ\text{C}$, unless otherwise specified.

Parameter		Symbol	Test Conditions	Min	Typ	Max	Unit	
Statistic Characteristics								
Collector-emitter Breakdown Voltage		BV_{CES}	$V_{GE}=0V, I_C=1mA$	700			V	
Gate Threshold Voltage		$V_{GE(th)}$	$V_{CE}=V_{GE}, I_C=250\mu A$	3.8	4.5	5.4	V	
Collector-emitter saturation voltage		V_{CEsat}	$V_{GE}=15V, I_C=100A,$ $T_J=25^\circ\text{C}$		1.57	1.9	V	
			$T_J=125^\circ\text{C}$		1.89		V	
			$T_J=175^\circ\text{C}$		2.08		V	
Zero Gate Voltage Collector Current		I_{CES}	$V_{CE}=650V, V_{GE}=0V$ $T_J=25^\circ\text{C}$		0.1	40	μA	
			$T_J=175^\circ\text{C}$			1	mA	
Gate-emitter Leakage Current	Forward	I_{GESF}	$V_{GE}=20V, V_{CE}=0V$			100	nA	
	Reverse	I_{GESR}	$V_{GE}=-20V, V_{CE}=0V$			-100	nA	
Dynamic Characteristics								
Input Capacitance		C_{IES}	$V_{CE}=25V, V_{GE}=0V,$ $f=1\text{ MHz}$		3750		pF	
Output Capacitance		C_{OES}			350			
Reverse Transfer Capacitance		C_{RES}			40			
Gate Resistance		R_G	$f=1\text{ MHz}, \text{Open Drain}$		1.7		Ω	
Turn-on Delay Time		$t_{d(on)}$	$T_J=25^\circ\text{C}$ $V_{CC}=400V, I_C=100A$ $R_G=10\Omega, V_{GE}=0/15V$ Energy losses include "tail" and diode reverse recovery		67		ns	
Rise Time		t_r			96		ns	
Turn-off Delay Time		$t_{d(off)}$			262		ns	
Fall Time		t_f			153		ns	
Turn-on energy		E_{on}			4.0		mJ	
Turn-off energy		E_{off}			1.4		mJ	
Total switching energy		E_{ts}			5.4		mJ	
Turn-on Delay Time		$t_{d(on)}$		$T_J=150^\circ\text{C}$ $V_{CC}=400V, I_C=100A$ $R_G=10\Omega, V_{GE}=0/15V$ Energy losses include "tail" and diode reverse recovery		67		ns
Rise Time		t_r				114		ns
Turn-off Delay Time		$t_{d(off)}$				289		ns
Fall Time		t_f			168		ns	
Turn-on energy		E_{on}			4.6		mJ	
Turn-off energy		E_{off}			1.8		mJ	
Total switching energy		E_{ts}			6.4		mJ	
Gate to Emitter Charge		Q_{GE}	$V_{CC}=400V, I_C=100A$ $V_{GE}=0\text{ to }15V$		42		nC	
Gate to Collector Charge		Q_{GC}			72			
Gate Charge Total		Q_G			162			

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Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Reverse Diode Characteristics						
Diode Forward Voltage	V_F	$I_F=50A$ $T_J=25^{\circ}C$		1.39	1.7	V
		$I_F=50A$ $T_J=125^{\circ}C$		1.23		
		$I_F=50A$ $T_J=175^{\circ}C$		1.13		
		$I_F=100A$ $T_J=25^{\circ}C$		1.71	2.0	
		$I_F=100A$ $T_J=125^{\circ}C$		1.62		
		$I_F=100A$ $T_J=175^{\circ}C$		1.55		
Reverse Recovery Time	t_{rr}	$T_J=25^{\circ}C$		190		ns
Reverse Recovery Charge	Q_{rr}	$V_R=400V, I_F=50A$		1150		nC
Peak Reverse Recovery Current	I_{rrm}	$dI_F/dt=840A/\mu s$		18		A
Reverse Recovery Time	t_{rr}	$T_J=25^{\circ}C$		174		ns
Reverse Recovery Charge	Q_{rr}	$V_R=400V, I_F=100A$		760		nC
Peak Reverse Recovery Current	I_{rrm}	$dI_F/dt=660A/\mu s$		14		A



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